PRODUCT ENGINEERING INTERN

Posting ID: IN19512435

Company: Cadence Design Systems

Position Type: Full-Time

Company Website: https://www.cadence.com/

Work Location: San Jose, CA

College Major(s): Electrical/Computer Engineering (EE/CpE)

Salary: DOE

College Level(s): Graduate Student, PhD.
Student, Alumni

OVERVIEW

Cadence hires and develops leaders and innovators who want to make an impact on the world of technology.

Cadence has a rich history of innovating design flows and solutions that benefit customers tremendously and help end users to be very efficient with their every day tasks.

Cadence plays a critical role in creating the technologies that modern life depends on. They are a global electronic design automation company, providing software, hardware, and intellectual property to design advanced semiconductor chips that enable our customers create revolutionary products and experiences.

Thanks to the outstanding caliber of the Cadence team and the empowering culture that they have cultivated for over 25 years, Cadence continues to be recognized by Fortune Magazine as one of the 100 Best Companies to Work For. Their shared passion for solving the world’s toughest technical challenges, their dedication to pushing the limits of the industry, and their drive to do meaningful work differentiates the people of Cadence.

Roles and Responsibilities

Building on the proven Virtuoso platform, Cadence has just released the Virtuoso RF Solution, a flow that specifically targets the RF module designs encompassing IC and packages/modules.

Virtuoso RF has introduced the industry's true IC-package co-design and allows designers to take the guesswork out of the design process. Additionally, Virtuoso RF had also integrated two Electromagnetic (EM) solvers very tightly within the design environment. Designers can use either one of these two EM solvers and save many hours because of in-built automation and bookkeeping of data.

Join us today and be part of the amazing team that is responsible for building additional capabilities within the Virtuoso RF flow. You will be able to get real-life design experiences by
using the EM tools for validating block and IC/package level layouts. You will also be able to shape the next release of Virtuoso RF by injecting fresh new ideas to the design cycle.

**Education and Qualifications**
A full-time student pursuing a MS/PhD in some combination of electrical/computer engineering, with preference for EM simulation background.

**Preferred Skills**
Experience of using industry standard EM tools is preferred.
Experience with real life RF or microwave designs is a plus.
Experience with rapid prototyping and concept development is beneficial.

**How to Apply**