PRODUCT ENGINEER INTERN

Posting ID: IN19A04455

Company: Renesas Electronics America Inc.

Position Type: Part-Time

College Major(s): Electrical/Computer Engineering (EE/CpE), Computer Science (CS)

Company Website: https://www.renesas.com/us/en/

Work Location: San Jose, CA

Salary: DOE

College Level(s): Graduate Student, PhD. Student

OVERVIEW

Renesas Electronics Corporation delivers trusted embedded design innovation with complete semiconductor solutions that enable billions of connected, intelligent devices to enhance the way people work and live. A global leader in microcontrollers, analog, power, and SoC products, Renesas provides comprehensive solutions for a broad range of automotive, industrial, home electronics, office automation, and information communication technology applications that help shape a limitless future.

Roles and Responsibilities
As our Test Engineer you will be creating and debugging ATE solutions (programs and hardware) for characterization, qualification and production of IDT devices.

Education and Qualifications
Basic Qualifications:

Develop and document test plan for new devices

Bring quality and cost effective test solution for mass production

Coordinate test related activities with both internal and external group

Evaluate new product’s testability and operability.

Design and debug test SW & HW for Production, Characterization & Reliability

Debug new silicon

Bring quality and cost effective test solution for mass production
Implement test programs and release into offsite production. You will go beyond the industry norm and demonstrate creativity in problem solving, ability to think dynamically and outside of standard convention, and adapt quickly to new technical areas.

**Education and/or Experience:**

BSEE / MSEE is required

**Preferred Skills**

- Hands-on experience working on Digital, Mixed Signal, RF, High-speed Digital (>10Gbps) and PMIC Devices on ATE (i.e. Teradyne iFlex/J750, Advantest 93k platforms.) and SLT platforms.

- Strong Programming skills for writing and debugging test programs and test related hardware development on ATE.

- Hands on experience designing ATE Hardware creating schematics and reviewing layouts.

- Hands on experience in Verilog to read and utilize device Verification Test Benches nice to have.

- Competency in programming with Scripting languages (i.e., PERL) and high level languages (i.e., C/C++ or Visual Basic.)


- In-depth knowledge of Semiconductor Manufacturing Process.

- Understanding of semiconductor product life cycle.

- Expertise in Semiconductor Test Methodology.

- Experience with automating bench test equipment (i.e. LabVIEW, Python, Visual Basic)

Will be working on providing effective ATE solutions.

Considering 0-5 years of experience for the role.

You should be a standout colleague; work well independently as well as collaboratively in a team setting.

**How to Apply**