Raytheon Company is a technology and innovation leader specializing in defense, civil government and cybersecurity solutions. Founded in 1922, Raytheon provides state-of-the-art electronics, mission systems integration, capabilities in C5I (command, control, communications, computing, cyber and intelligence), sensing, effects and mission support services.

Roles and Responsibilities
The Systems Test Directorate is seeking candidates for the Integration & Verification Center (IVC). IVC supports integration of hardware, software, and configurable logic using multiple test equipment on a variety of platforms. Integration and test activities occur in a laboratory or on a test range with live hardware, inert hardware and/or simulators. Data capture and analysis is performed to evaluate total system level performance, producibility, and reliability to ensure the highest quality product is delivered to the warfighter.

JOB DESCRIPTION:
The candidate will participate on engineering teams to perform integration, functional tests, verification, and validation tests of RMS products as well as design, development, and integration of test solutions.

Tasks may include the following activities:
- Perform laboratory integration and test activities
- Perform system level integration and field/flight test activities
- Evaluate system performance through test data analysis
- Develop subsystem and system test requirements, analysis and plans

Education and Qualifications
REQUIRED SKILLS:
- U.S. Citizenship
- Ability to obtain Secret Security Clearance
• Basic engineering skills (design, troubleshooting, problem solving, data analysis, etc)
• Familiar with laboratory equipment, electronics and software (i.e. oscilloscope, DMM, Spectrum analyzer, MatLab, etc)
• Computer skill (PowerPoint, Excel, etc)
• Good verbal and written communication skills

DESIRED SKILLS:
• Able to work on diverse teams and have the ability to multitask
• Knowledge in developing and evaluating hardware and test requirements
• Understanding of physical, mechanical, electrical, and logical interfaces between components, units under test (UUTs) and test platforms (i.e. MIL-STD-1553)
• Knowledge in developing and executing system level tests to verify requirements
• Knowledge in component & system level testing, troubleshooting, failure analysis, root cause & corrective action processes
• Knowledge in System level test planning, data collection & data analysis

REQUIRED EDUCATION:
• Bachelor or Master of Science in Engineering field (Electrical, Computer, Mechanical, Aerospace, Systems or a related discipline) from an ABET accredited curriculum with a minimum GPA of 3.0. Must have graduated within the past 18 months or projected to graduate in the next 12 months.

How to Apply