INTERNSHIPS, ALDEC

Company: Aldec
Company Website: www.aldec.com
Job Title: Intern
Work Location: Henderson, NV
Position Type: Part-Time, 15-20 hours, 2 openings
Salary: $15/hr
College Level: Juniors in Electrical Engineering, Computer Engineering

OVERVIEW
Interns will learn FPGAs, design, verification, HDL coding standards, traceability and FPGA tools, and will be familiar with the overall Electronic Design Automation (EDA) industry. We have two open positions for this now. Intern candidates have to meet the following criteria:

a. Junior level, Computer Engineering or Electrical Engineering major
b. >= GPA 3.0
c. Knows basic VHDL/Verilog and FPGAs

To Apply

Send resumes to: satyamj@aldec.com